

LESSON PLAN

Name of the Faculty: Kulbhushan Bansal

Discipline: Computer Engineering

Department: Computer Engineering

Semester: 4th

Subject: Computer Organization

****Work load (Lecture / Practical) per week (in hours): Lectures-03**

Week	Theory	
	Lecture Day	Topic (including assignments /tests)
1 st Week	1 st	Hardware organisation of computer system Basic Principle: Basic about Computer System
	2 nd	CPU organization : general register organisation
	3 rd	Stack organization
	4 th	Instruction formats : Introduction
Week 2	1 st	Three address, two address,
	2 nd	One address, zero address
	3 rd	RISC instruction
	4 th	Addressing modes: Immediate, register
Week 3	1 st	Direct, in direct,.
	2 nd	Relative, indexed
	3 rd	CPU Design: MicroProgramdvs hard wired control.
	4 th	CPU Design: MicroProgramdvs hard wired control.
Week 4	1 st	Reduced instruction set computers Reduced instruction set computers
	2 nd	Reduced instruction set computers Reduced instruction set computers
	3 rd	CISC characteristics
	4 th	RISC characteristics,
Week 5	1 st	Comparison between CISC & RISC
	2 nd	Assignment on CPU Organization
	3 rd	Assignment on CPU Design
	4 th	Discussion on unit 1
Week	1 st	Memory organization: Basics About Memory

6	2 nd	Memory Hierarchy
	3 rd	RAM and ROM chips
	4 th	Memory address map
Week 7	1 st	Memory connections to CPU
	2 nd	Auxillary memory : Magnetic disks
	3 rd	Auxillary memory : magnetic tapes
	4 th	Associative memory
Week 8	1 st	Cache memory
	2 nd	Virtual memory
	3 rd	Memory management hardware
	4 th	Assignment on Memory Hierarchy
Week 9	1 st	Assignment on Auxillary memory
	2 nd	Test
	3 rd	I/O organization: Basis Input output system(BIOS)
	4 th	Function of BIOS
Week 10	1 st	Testing and initialization
	2 nd	Configuring the system
	3 rd	Assignment on BIOS
	4 th	Modes of Data Transfer
Week 11	1 st	Programd I/O
	2 nd	Synchronous, asynchronous and interrupt initiated
	3 rd	Synchronous, asynchronous and interrupt initiated
	4 th	DMA data transfer
Week 12	1 st	Assignment on modes of Data Transfer
	2 nd	Test
	3 rd	Architecture of multiprocessor systems : Introduction about Multi processor systems
	4 th	Architecture of multiprocessor systems
Week 13	1 st	Forms of parallel processing
	2 nd	Parallel processing and pipelines
	3 rd	Basic characteristics of multiprocessor
	4 th	Assignment on multiprocessor System

Week 14	1 st	General purpose multiprocessors'
	2 nd	Interconnection networks : time shared common bus
	3 rd	multi port memory
	4 th	cross bar switch
Week 15	1 st	multi stage switching networks and hyper cube structures
	2 nd	multi stage switching networks and hyper cube structures
	3 rd	Assignment on Interconnection networks
	4 th	Test